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Application Number	TBD	
	Filing Date	Herewith
	First Named Inventor	
	Art Unit	NA
	Examiner Name	NA
Attorney Docket Number	P05763 (11461.00.5763)	

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OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		PETER VANCORENLAND and MICHIEL S.J. STEYAERT, A 1.57-GHz Fully Integrated Very Low-Phase-Noise Quadrature VCO, IEEE Journal of Solid-State Circuits, Vol. 37, No. 5, May 2002	
		PEPIJN VAN DE VEN, JOHAN VAN DER TANG, DIETER KASPERKOVITZ, ARTHUR VAN ROERMUND, An Optimally Coupled 5 GHz Quadrature LC Oscillator, 2001 Symposium on VLSI Circuits Digest of Technical Papers	
		PIETRO ANDREANI, ANDREA BONFANTI, LUCA ROMANO and CARLO SAMORI, Analysis and Design of a 1.8-GHz CMOS LC Quadrature VCO, IEEE Journal of Solid-State Circuits, Vol. 37, NO. 12, December 2002	

Examiner Signature		Date Considered	6/20/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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